





S/N 09/476,622 <u>D STATES PATENT AND TRADEMARK OFFICE</u>

Examiner: William T. Treat

Group Art Unit: 2183

Docket: 884.101US1

Serial No.: Filed:

December 31, 1999

Howard Chin et al.

09/476,622

Title:

Commissioner for Patents Washington, D.C. 20231

Applicant:

EXTERNAL MICROCODE

<u>AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111</u>

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APR 1 5 2002

Technology Center 2100

The Applicant has reviewed the Office Action mailed on September 27, 2001. This response is accompanied by a Petition, as well as the appropriate fee, to obtain a 3-month extension of the period for responding to the Office action, thereby moving the deadline for response from December 27,2001 to March 27, 2002. Please amend the above-identified patent application as follows.

IN THE SPECIFICATION

Please make the paragraph substitutions indicated in the appendix entitled Clean Version of Amended Specification Paragraphs. The specific changes incorporated in the substitute paragraphs are shown in the following marked-up versions of the original paragraphs:

Please amend the paragraph beginning on page 2, line 19, as follows:

Some embodiments of the invention include a computer system comprising a bus, a processor and a computer readable medium external to the processor. The computer readable medium is coupled to the processor by the bus and [the computer readable medium to store] stores instructions to implement the microcode functions.

Please amend the paragraph beginning on page 2, line 28, as follows:

FIG. 1 is a block diagram of an example embodiment of a system according to the present invention.

Please amend the paragraph beginning on page 6, line 19, as follows:

The CRAB 306 provides communication links between the functional units 308a-308k of cessor 304 and the data control unit 310. The data control unit 310 executes the various tions provided to control the operations of the system. In one embodiment, the data unit [306] 310 fetches an instruction from memory or from firmware or from any other the processor 304 and the data control unit 310. The data control unit 310 executes the various instructions provided to control the operations of the system. In one embodiment, the data control unit [306] 310 fetches an instruction from memory or from firmware or from any other

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